



Form PTO-1449 (modified)

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INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Atty. Docket No.
2000.039500/TT3768Serial No.
09/853,465Applicant
Geoffrey S. StronginFiling Date:
May 11, 2001Group:
2604U.S. Patent Documents
*See Page 1*Foreign Patent Documents
*See Page 1*Other Art
See Page 1

U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date of App.
	A1						
	A2						RECEIVED
	A3						OCT 08 2002
	A4						Technology Center 2600
	A5						

Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	B1						
	B2						RECEIVED
	B3						OCT 09 2002
	B4						Technology Center 2100
	B5						

Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
/ET/	C1	Intel, "Low Pin Count (LPC) Interface Specification Revision 1.0," pp. 1-31 (09/29/97)
	C2	Standard Microsystems Corporation, "100 Pin Enhanced Super I/O for LPC Bus with SMBus Controller for Commercial Application," Part No. LPC47B37x, pp. 1-254 (06/17/99)
	C3	FIPS Pub 140-1 Federal Information Processing Standards Publication, "Security Requirements for Cryptographic Modules" (01/11/1994)
	C4	Intel, "Communication and Networking Riser Specification," Revision 1.0 (02/07/2000)
	C5	"Handbook of Applied Cryptography" CRC Press 1997 pp. 154 - 157, 160 - 161, 191 - 198, 203 - 212

EXAMINER: /Ellen Tran/

DATE CONSIDERED: 08/02/2007

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